
	Nutan Maharashtra Vidya Prasarak Mandal's		Record No.: ACDM/R/08
	Nutan Maharashtra Institute of Engineering & Technology		Revision: 00
	Under Administrative Support of Pimpri Chinchwad Education Trust		Date: 15.06.2016
<h2>Notice</h2>			

Department: **E&TC**

Academic Year: **2018- 2029**

Semester: **I**

Year: **BE**

Date: **11/8 /2018**

All BE E&TC students are hereby informed that one day hands on "VLSI Design and Technology" is scheduled on 18/8/2018. Attendance is compulsory to all.

Bhagyashri Warhade



Event Coordinator: **Prof. Bhagyashri Warhade**

D. K. Joshi

HOD

V. K.
Principal
Nutan Maharashtra Institute
of Engg. & Technology
"Samarth Vidya Sankul" Vishnupuri
Talegaon Dabhade, 410507



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Report of the event organized			

Department: E&TC

Academic Year: 2018 - 2019

Date: 1/09/18

To,

The Principal,

Through: The HOD E&TC

Reference:

Subject: Submission of the Report of One day hands on "VLSI Design and Technology" for BE (E&TC) students.

Respected Sir,

With above reference, we have successfully organized One day hands on "VLSI Design and Technology" for BE (E&TC) students on **18/08/2018**. The details are given below. The more detailed report (Documents, photographs, feedback of students) is enclosed herewith.

No. of Participants: 20

Faculty Coordinator: Prof. Bhagyashri A. Warhade

Sr. No.	Date	Title	Important details of events
1	18/08/2018	"Hands on VLSI Design and Technology"	<ul style="list-style-type: none"> ➤ A session was started with Introduction of Resource Person Prof. Harish Thakar. ➤ The main objectives of program were: ➤ To get familiar with Spartan 3E kits and Microwind software. ➤ How to write Xilinx program and how to implement it in FPGA.

Principal
 Nutan Maharashtra Institute
 of Engg. & Technology
 "Samarth Vidya Sanstha" Vichhapuri
 Talgaon Bhande, 410507



			<ul style="list-style-type: none"> ➤ To prepare CMOS layout in selected technology, simulate with and without capacitive load, comment on rise, and fall times. ➤ The event was started at 9.30am and completed at 4.30pm.
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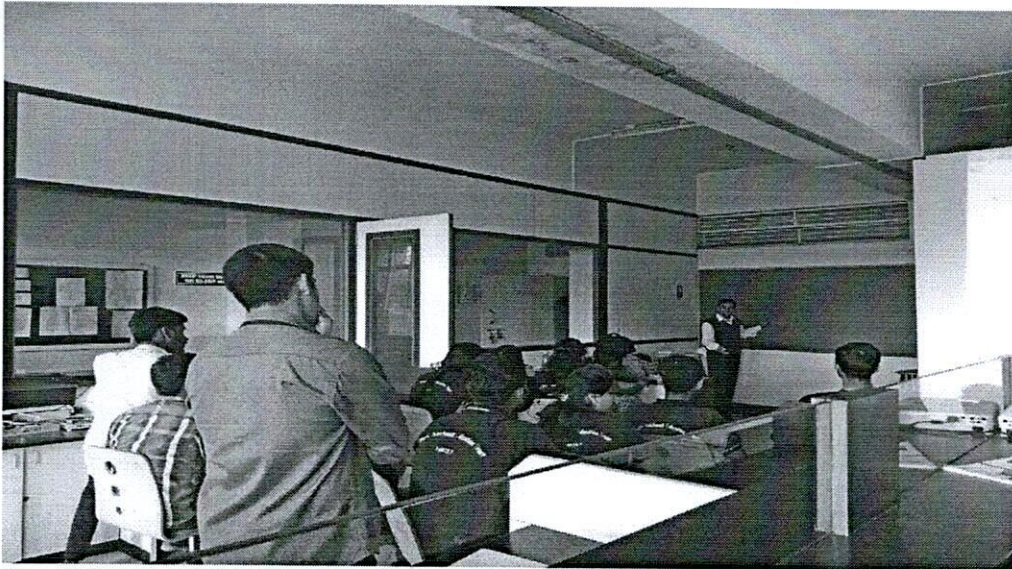
Thanking You.

BBT akhah

Event Coordinator

Encl: Detailed report of event prepared by students

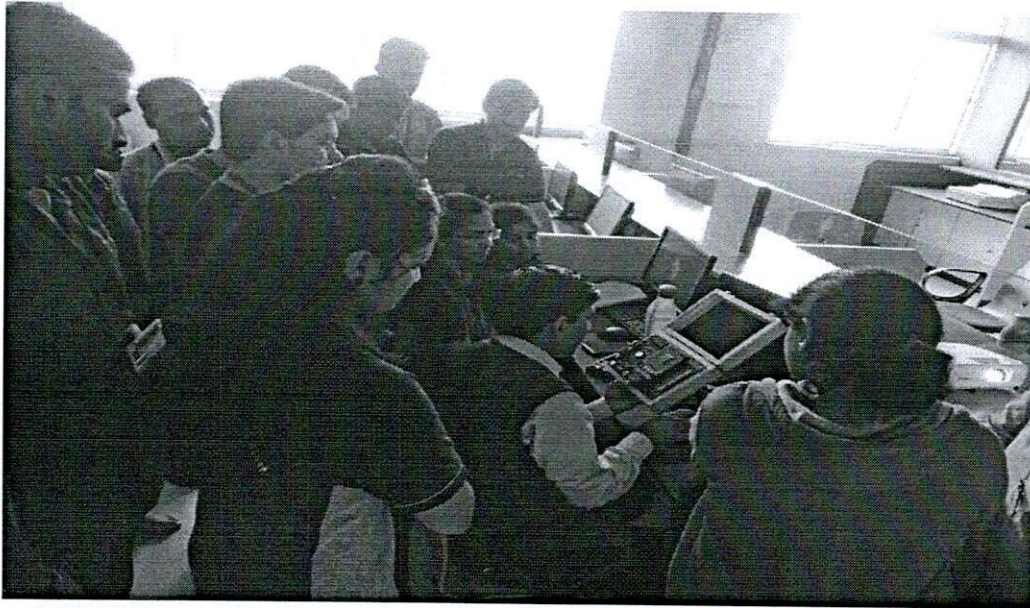
Photos:



Date: 18/8/2018 Speaker: Prof. Harish Thakar Session: one day hands on VLSI Design and Technology

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Talgaon Dabhade, 410507





Date: 18/8/2018 Speaker: Prof. Harish Thakar Session: one day hands on VLSI Design and Technology



Harish

Event Coordinator

Harish

HOD (E&TC)

Le
Principal
Nutan Maharashtra Institute
of Engg. & Technology
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Talgaon Dabhade, 410507





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Trust



Record No.:
ACDM/R/08
Revision: 00
Date:
15.06.2016

Department of E&TC Engineering

Event Name : one day hands on VLSI Design and Technology		Date : 16/8/2018
Sr.No.	Name of Student	Sign
1	BHOTE NIKITA BALASAHEB	B. N. B
2	DIGHE SANKET KISHOR	Dighe
3	HAJARE ANUSAYA MUKAND	Hajare
4	KAMBLE VIDYA ANIL	Vidya
5	KARANDE SANTOSH ASHOK	Karande
6	KATE SUPDA SHATRUGHAN	Kate
7	MATTOO SAHIL NANAJI	Sahil
8	MOTE ASHWINI ASHOK	Ashwini
9	MURUMKAR ROHINI HANUMANT	Rohini
10	PATIL SMITA SOMSHANKAR	Smitta
11	RAM PRAVIN VIRENDRA	RAM
12	RASIKA SANJAY VANAGUTTE	Rasika
13	RASKAR PUNAM SHRIRANG	Raskar
14	SHEVKARI PRACHI RAM	S. P. R
15	ZINGADE KESHAV MANOHAR	Keshav
16	ANSARI SHAHBAZ SHAKEEL AHMED	Ansari
17	MAJARE SHRADDHA SWAMINATH	M. S. S
18	MANE PATIL JYOTI SHAHAJI	Jyoti
19	NAIDU ASHOK KUMAR MUNNUSWAMY	Naidu
20	NIKAM PADMINI SHANKAR	Ni. Kam

Event Coordinator

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Principal
Nutan Maharashtra Institute
of Engg. & Technology
"Samarth Vidya Sankul" Vishnupuri
Bhamburda Dabhade, 410507