



Nutan Maharashtra Vidya Prasarak Mandal's  
Nutan Maharashtra Inst. of Engg. & Tech.

Record No.: ACDM/R/


Revision: 00

Date:

### Schedule Of The Workshop

Name of Workshop: Workshop On "Embedded Systems" Date of Workshop : 08.09.2018 to 10.09.2018

Day & Date	Time	Particulars	Venue
Day1 08.09.18	9.00am -9.30 am	Inaguration	<del>seminar</del> Hall/R-203
	9.30am . -11.00 am	Session-I on Overview of Embedded system, Architecture of 8051, Ports and Timers of 8051	R-203
	11.00am -11.15 am	Tea Break	
	11.15 am-12.45 pm	Session-II Interrupts of 8051 ,Interfacing of 7- segment Display, LCD & K/B with 8051	R-203
	12.45 pm- 1.15 pm	Lunch Break	
	1.15pm- 2.15 pm	Session-III Interfacing of ADC,DAC and Stepper motor with 8051	R-203
	2.45 pm – 3.00 pm	Tea Break	
	3.00 pm-4.30 pm	Session IV Practical session on 8051	R-208
Day2 09.09.18	9.30 am. -11.00 am	Session-I Architecture of PIC 18FXX, Ports and Timers of 18FXX	R-203
	11.00am -11.15 am	Tea Break	
	11.15 am-12.45 pm	Session-II Interfacing of LCD,ADC,DAC with PIC18Fxx	R-203
	12.45 pm- 1.15 pm	Lunch Break	
	1.15pm- 2.15 pm	Session-III I2C and SPI communication protocols and their interfacing .	R-203
	2.45 pm – 3.00 pm	Tea Break	
	3.00 pm-4.30 pm	Session IV Practical session on PIC18Fxx	R-208

  
Principal  
Nutan Maharashtra Institute  
of Engg. & Technology  
"Samarth Vidya Sanstha" Vishnupuri  
Talegaon Dabhade, 410507



Day & Date	Time	Particulars	Venue
Day3 10.09.18	9.30 am. -11.00 am	Session-I Introduction and Architecture of ARM, Comparison of 8051,PIC18Fxx and ARM	R-203
	11.00am -11.15 am	Tea Break	
	11.15 am-12.45 pm	Session-II , Ports ,Timers, Interrupts of ARM	R-203
	12.45 pm- 1.15pm	Lunch Break	
	1.15pm- 2.45 pm	Session-III Practical session on 8051 and PIC	R-208
	2.45pm - 3.00pm	Tea break	
	3.00pm-4.15pm	Session IV Practical session on 8051 and PIC	
	4.15 pm -4.30pm	Vote of thanks and Certificate Distribution	R-203

S. Patil  
Prof. S. N. Patil  
Co-ordinator.